

CLAIMS

1. A semiconductor memory card comprising:

a first storage unit operable to store data, the first storage unit having characteristics by which the data becomes uncertain in the first storage unit the after the data is read out;

a second storage unit operable to store processing mode specification information that specifies a mode of writing into each address of the first storage unit after the stored data is read out;

a reading unit operable to read out the data stored in a designated address of the first storage unit;

a processing mode determination unit operable to determine a mode of writing into the designated address, comparing the designated address with the processing mode specification information, when the stored data is read out by the reading unit; and

a writing unit operable alternatively to write or not to write certain data into the designated address according to the mode determined by the processing mode determination unit after the data stored in the designated address is read out.

2. A semiconductor memory card according to Claim 1,

wherein the writing unit is operable to write the certain data that is a specific value into the designated address.

3. A semiconductor memory card according to Claim 1, further comprising a random number generation unit operable to generate a random number,

wherein the writing unit is operable to write the certain data that is the random number generated by the random number generation unit into the designated address.

4. A semiconductor memory card according to Claim 1,
wherein the writing unit is operable to write the certain data
that is read out by the reading unit into the designated address.

5 5. A semiconductor memory card according to Claim 1,
wherein the designated address is different from an address
to be processed by the reading unit and the writing unit.

6. A semiconductor memory card according to Claim 1,
10 wherein a specific part of the designated address is used for
determining the mode of writing.

7. A method for processing a storage unit after data is read out,
in a semiconductor memory card including the storage unit operable
15 to store the data, the storage unit having characteristics by which
the data becomes uncertain in the storage unit after the data is read
out, the method comprising:

reading out the data stored in a designated address of the
storage unit;

20 determining a mode of writing into the designated address,
comparing the designated address with processing mode
specification information that specifies the mode of writing into each
address of the storage unit after the stored data is read out, when
the stored data is read out in the reading out; and

25 alternatively writing or not writing certain data into the
designated address according to the mode determined in the
determining of the mode of writing after the data stored in the
designated address is read out.

30 8. An integrated circuit in a semiconductor memory card
including a storage unit operable to store data, the storage unit
having characteristics by which the data becomes uncertain in the

storage unit after the data is read out, the integrated circuit comprising:

a reading unit operable to read out the data stored in a designated address of the storage unit;

5 a processing mode determination unit operable to determine a mode of writing into the designated address, comparing the designated address with processing mode specification information that specifies the mode of writing into each address of the storage unit after the stored data is read out, when the stored data is read
10 out by the reading unit; and

a writing unit operable alternatively to write or not to write certain data into the designated address according to the mode determined by the processing mode determination unit after the data stored in the designated address is read out.

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9. A program for processing a storage unit after data is read out, in a semiconductor memory card including the storage unit operable to store the data, the storage unit having characteristics by which the data becomes uncertain in the storage unit after the data is read
20 out, the program causing a computer to execute:

reading out the data stored in a designated address of the storage unit;

determining a mode of writing into the designated address, comparing the designated address with processing mode
25 specification information that specifies the mode of writing into each address of the storage unit after the stored data is read out, when the stored data is read out in the reading out; and

alternatively writing or not writing certain data into the designated address according to the mode determined in the
30 determining of the mode of writing after the data stored in the designated address is read out.

10. A storage medium that stores a program for processing a storage unit after data is read out, in a semiconductor memory card including the storage unit operable to store the data, the storage unit having characteristics by which the data becomes uncertain in the storage unit after the data is read out, the program causing a computer to execute:

reading out the data stored in a designated address of the storage unit;

determining a mode of writing into the designated address, comparing the designated address with processing mode specification information that specifies the mode of writing into each address of the storage unit after the stored data is read out, when the stored data is read out in the reading out; and

alternatively writing or not writing certain data into the designated address according to the mode determined in the determining of the mode of writing after the data stored in the designated address is read out.